List of reference symbols

Identical reference symbols denote identical or functionally identical components or steps in the figures.

100	Delay device
101	First delay element
102	Second delay element
103	Input signal
104, 104a	Output signal
105	Variable time delay
106	Feedback device
107	Fed-back output signal
108	Phase difference detection device
109	Control signal
109'	Filtered control signal
110	Frequency detection unit
111	Filtering device
201	Receiver time delay
202	Path time delay
203	Driver time delay
204	Overall time delay
205	Delay units
206	Frequency detection start
207	Frequency detection end
208	Delay control mode
209	Reset pulse`
210	Overlap area
211, 212	Detection frequency ranges
213	Low frequency
214	Intermediate frequency
215	High frequency
216	Even-numbered DDR clock flank
217	Odd-numbered DDR clock flank
218	Even-numbered DDR data
219	Odd-numbered DDR data
220	Clock signal
221	Asynchronous data stream

301, 302, 303	Inverter element
304	Control voltage
305	Variable capacitor element
306	Fixed voltage
401, 401	Inverter element bank
403	Clock input signal
404	Inverted clock input signal
405	Clock output signal
406	Inverted clock output signal
407 – 410	NAND gates
411	Clock generator
412	Reference delay device
412a – 412d	Reference delay elements
413	Locking gate
414	Delayed clock input signal
415	Clock generator output signal
416	Gate signal
417	Flank difference time